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Introduction. <p>Silicon Carbide Microsystems for Harsh Environments reviews state-of-the-art Silicon Carbide (SiC) technologies that, when combined, create microsystems capable of surviving in harsh environments. Technological readiness of the system components, key issues when integrating these components into systems, and other hurdles in harsh environment operation are discussed at length.

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semiconductor silicon carbide, SiC, enables device operation to temperatures in excess of 1200 K. In the case of the 6H polytype the energy gap is 3.0 eV, compared to 1.1 eV for silicon. These field-effect devices require a robust dielectric to enable modulation of the semiconductor carrier concentration via an applied gate potential.

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The material properties of various forms of SiC (single crystalline, polycrystalline, and amorphous) along with their use for creating the various components of harsh environment microsystems will also be discussed. Current status and future research are highlighted with regards to both materials and processing technologies.

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